

100G QSFP28 to 4x25G SFP28 Active Optical Cables

The Four-Channel, Pluggable, Parallel, Fibre-Optic 100G QSFP28 Active Optical Cable (AOC) to 4x25G SFP28 Active Optical Cable break-out solution is intended for 100G to 4x25G applications. This AOC is a high-performance cable for short-range multi-lane data communication and interconnect applications. Datatronix 100G QSFP28 to 4x 25G SFP28 Active Optical Cable is designed for use in breakout to 4x 25G Ethernet links up to 100m on Multi-Mode Fibre (MMF). Based on vertically integrated VCSEL array technology and designed with QSFP28/SFP28 MSA-compliant high-density connectors, the 100G QSFP28 to 4x 25G SFP28 AOC assemblies are compact, lightweight, and low power.

Applications

- 100GbE and 25GbE break-out applications for Datacom switch and router connections
- 100G to 4x25G density applications for Datacom and Proprietary protocol applications
- Data centres

Features

- Electrical interface compliant to SFF-8436 and SFF-8431
- 850nm VCSEL laser and PIN photo-detector
- Built-in digital diagnostic functions
- Operating case temperature 0°C to 70°C
- Hot Pluggable
- RoHS compliant

Specifications

Absolute Maximum Ratings

ELEMENT	VALUE	SYMBOL	MIN	MAX
Storage Temperature	°C	T _S	-20	85
Relative Humidity	%	R _H	0	85
Case Operating Temperature	°C	T _{Case}	0	70
Supply Voltage	V	V _{CC}	-0.3	3.6

Recommended Operating Conditions

ELEMENT	VALUE	SYMBOL	MIN	TYPICAL	MAX
Case Operating Temperature	°C	T _{Case}	0		70
Supply Voltage	V	V _{CC}	3.13	3.3	3.47
Supply Current (QSFP28)	mA	I _{CC}			1000
Supply Current (SFP28)	mA	I _{CC}			300
Data Rate Per Lane	Gbit/s	DR		25.78125	

Transmitter Specifications

Measurement condition: Channel data rate 25.78125Gbps, VCC=3.3V, PRBS31 pattern, Case operating temperature 0-70°C

ELEMENT	VALUE	SYMBOL	MIN	TYPICAL	MAX
QSFP28					
Center wavelength	nm	λ _c	840	850	860
Differential Input Impedance	Ohm	Z _{in}	90	100	110
Differential Input Voltage	mVp-p	V _{in}	300		1100
Average Launch Power per Lane	dBm	P _{AVG}	-7.5	-1	+2.5
Extinct Ratio	dB	ER	2.0		
SFP28					
Center wavelength	nm	λ _c	840	850	860

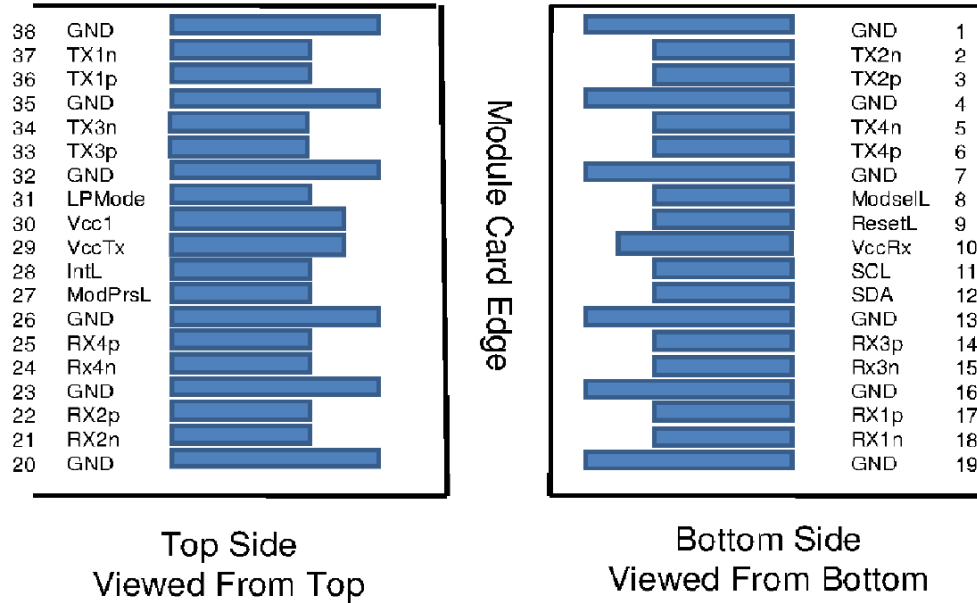
Differential Input Impedance	Ohm	Z_{in}	90	100	110
Differential Input Voltage	mVp-p	V_{in}	300		1100
Average Launch Power per Lane	dBm	P_{AVG}	-7.5	-1	+2.5
Extinct Ratio	dB	ER	2.0		

Receiver Specifications

Measurement condition: Channel data rate 25.78125Gbps, VCC=3.3V, PRBS31 pattern, Case operating temperature 0-70°C

ELEMENT	VALUE	SYMBOL	MIN	TYPICAL	MAX
QSFP28					
Center wavelength	nm	λ_c	840	850	860
Differential Output Impedance	Ohm	Z_{out}	90	100	110
Differential Output Voltage	mVp-p	V_{out}	500		800
Receiver Overload	dBm	S_{EN}	+2.5		
Bit Error Rate		BER			10^{-12}
SFP28					
Center wavelength	nm	λ_c	840	850	860
Differential Output Impedance	Ohm	Z_{out}	90	100	110
Differential Output Voltage	mVp-p	V_{out}	500		800
Receiver Overload	dBm	S_{EN}	+2.5		
Bit Error Rate		BER			10^{-12}

QSFP28 Pin Descriptions

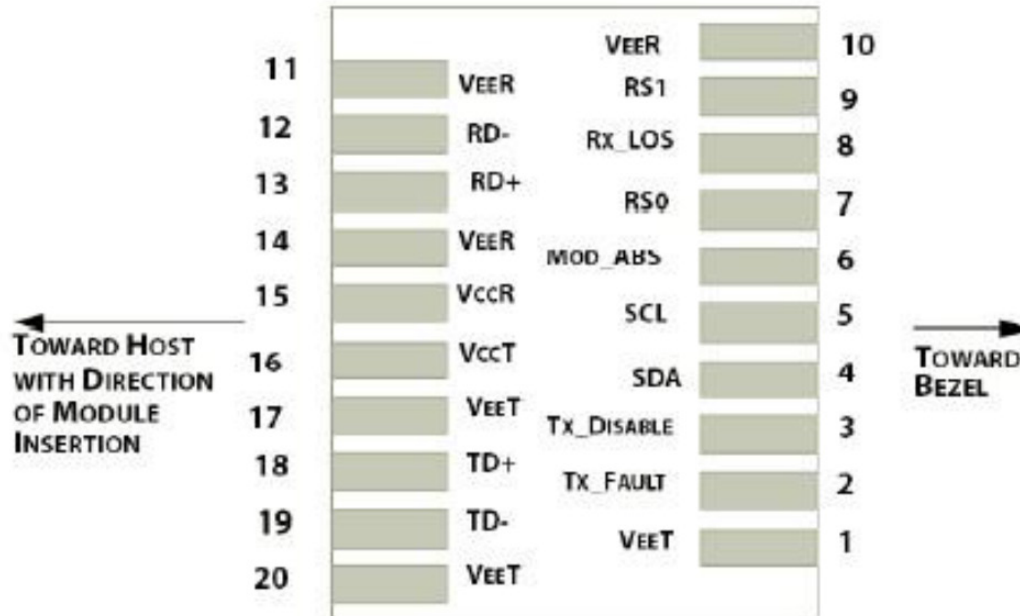


Pin Definitions

PIN	SYMBOL	NAME/DESCRIPTION
1	GND	Ground
2	Tx2n	Transmitter Inverted Data Input
3	Tx2p	Transmitter Non-Inverted Data Input
4	GND	Ground
5	Tx4n	Transmitter Inverted Data Input
6	Tx4p	Transmitter Non-Inverted Data Input
7	GND	Ground
8	ModSelL	Module Select
9	ResetL	Module Reset
10	Vcc Rx	+3.3 V Power supply receiver
11	SCL	2-wire serial interface clock
12	SDA	2-wire serial interface data
13	GND	Ground
14	Rx3p	Receiver Non-Inverted Data Output
15	Rx3n	Receiver Inverted Data Output
16	GND	Ground

17	Rx1p	Receiver Non-Inverted Data Output
18	Rx1n	Receiver Inverted Data Output
19	GND	Ground
20	GND	Ground
21	Rx2n	Receiver Inverted Data Output
22	Rx2p	Receiver Non-Inverted Data Output
23	GND	Ground
24	Rx4n	Receiver Inverted Data Output
25	Rx4p	Receiver Non-Inverted Data Output
26	GND	Ground
27	ModPrsL	Module Present
28	IntL	Interrupt
29	Vcc Tx	+3.3 V Power supply transmitter
30	Vcc1	+3.3 V Power Supply
31	LPMode	Low Power Mode
32	GND	Ground
33	Tx3p	Transmitter Non-Inverted Data Input
34	Tx3n	Transmitter Inverted Data Input
35	GND	Ground
36	Tx1p	Transmitter Non-Inverted Data Input
37	Tx1n	Transmitter Inverted Data Input
38	GND	Ground

SFP28 Pin Descriptions

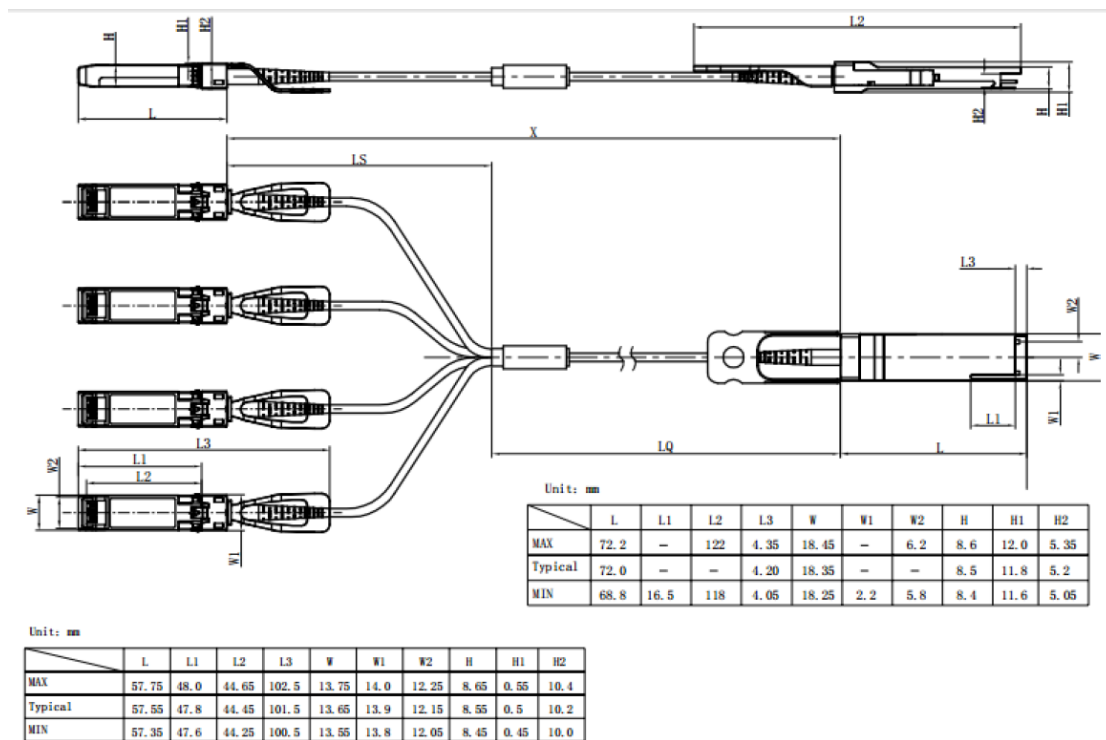


Pin Definitions

PIN	SYMBOL	NAME/DESCRIPTION
1	VeeT	Transmitter Signal Ground
2	TX_FAULT	Transmitter Fault (LVTTTL-O) – Not used. Grounded inside the module
3	TX_DISABLE	Transmitter Disable (LVTTTL-I) – High or open disables the transmitter
4	SDA	Two Wire Serial Interface Data Line (LVCMOS – I/O) (same as MOD-DEF2 in INF-8074)
5	SCL	Two Wire Serial Interface Clock Line (LVCMOS – I/O) (same as MOD-DEF1 in INF-8074)
6	MOD_ABS	Module Absent (Output), connected to VeeT or VeeR in the module
7	RS0	Rate Select 0 - Not used, Presents high input impedance.
8	RX_LOS	Receiver Loss of Signal (LVTTTL-O)
9	RS1	Rate Select 1 - Not used, Presents high input impedance.
10	VeeR	Receiver Signal Ground
11	VeeR	Receiver Signal Ground
12	RD-	Receiver Data Out Inverted (CML-O)
13	RD+	Receiver Data Out (CML-O)
14	VeeR	Receiver Signal Ground
15	VccR	Receiver Power + 3.3 V

16	VccT	Transmitter Power + 3.3 V
17	VeeT	Transmitter Signal Ground
18	TD+	Transmitter Data In (CML-I)
19	TD-	Transmitter Data In Inverted (CML-I)
20	VeeT	Transmitter Signal Ground

Mechanical Specifications



Ordering Information

DESCRIPTION

PART NUMBER

Datatronix 100G QSFP28 to 4x25G SFP28 Active Optical Cable

QSFP-SFP-AOC-100425-XXX-YYY

*where XXX is trunk cable length in metres & YYY is branch cable length in metres
Total cable length = Trunk length + Branch length